|  | DEPARTMENT OF ELECTRICAL ENGINEERING ORISSA SCHOOL OF MINING ENGINEERING KEONJHAR <br> Website- www.osme.co.in Email- osmeelectricaldept@gmail.com |  |  |  |
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| LESSON PLAN |  |  |  |  |
| Discipline- ELECTRICAL ENGINEERING | Semester5th | Name of the Teaching Faculty- ER. SHRADHA PATRA |  |  |
| Subject- DIGITAL ELECTRONICS \& MICROPROCESSOR LAB Subject Code- Pr3 | No. of days/week lab allotted01 | Semester From Date: 01/08/2023 To Date: 30/11/2023 Number of weeks- 18 |  |  |
| MONTH | WEEK | NO. OF PERIODS AVAILABLE | CLASS DAY | EXPERIMENTS TO BE CONDUCTED |
| AUGUST | 1st | 12 | 07/08/2023 | Verify truth tables of AND, OR, NOT, NOR, NAND, XOR, XNOR gates |
|  | 2nd |  | 14/08/2023 | Implement various gates by using universal properties of NAND \& NOR gates and verify truth table. |
|  | 3rd |  | 21/08/2023 | Implement half adder and Full adder using logic gates. <br> Implement half subtractor and Full subtractor using logic gates |
|  | 4th |  | 28/08/2023 | Implement a 4-bit Binary to Gray code converter. <br> Implement a Single bit digital comparator |
|  | 5th | 12 | 04/09/2023 | Study Multiplexer and demultiplexer |
| SEPTEMBER |  |  |  | Study of flip-flops. |
|  | 6th |  | 11/09/2023 | i) S-R flip flop ii) JK flip flop iii) flip flop iv) T flip flop |
|  | 7th |  | 18/09/2023 | Realize a 4-bit asynchronous UP/Down counter with a control for up/down counting. |
|  | 8th |  | 25/09/2023 | Realize a 4-bit synchronous UP/Down counter with a control for up/down counting. |



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